

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	1067	330/253.ccls.	USPA T	2006/02/ 02 18:55			
2	BRS	L2	840	330/257.ccls.	USPA T	2006/02/ 02 18:57			
3	BRS	L3	92	330/269.ccls.	USPA T	2006/02/ 02 18:57			
4	BRS	L4	749	330/261.ccls.	USPA T	2006/02/ 02 18:57			
5	BRS	L5	17396	(activat\$ or enabl\$ or on or switch\$) near2 bias\$	USPA T	2006/02/ 02 19:14			
6	BRS	L6	47085	differential near2 amplif\$	USPA T	2006/02/ 02 19:15			
7	BRS	L7	15689	pmos near2 transistor\$	USPA T	2006/02/ 02 19:15			
8	BRS	L8	17847	nmos near2 transistor\$	USPA T	2006/02/ 02 19:15			
9	BRS	L9	503276	bias\$	USPA T	2006/02/ 02 19:16			
10	BRS	L10	217049 1	control or controlled or controller\$ or controlling	USPA T	2006/02/ 02 19:16			
11	BRS	L11	1337	L6 and L7 and L8 and L9 and L10	USPA T	2006/02/ 02 18:59			
12	BRS	L12	143	L11 and 5	USPA T	2006/02/ 02 19:00			

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
13	BRS	L1 3	4563	(activate or enable or on or switch) near2 bias	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:15			
14	BRS	L1 4	51769	differential near2 (amplifier or amplifiers)	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:15			

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
15	BRS	L1 5	11592	pmos near2 transistor	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:15			
16	BRS	L1 6	16865	nmos near2 (transistor or transistors)	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:15			

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
17	BRS	L1 7	14779	pmos near2 (transistor or transistors)	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:15			
18	BRS	L1 8	348066	bias	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:16			

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
19	BRS	L1 9	608834 0	control or controlled or controller\$ or controlling	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:16			
20	BRS	L2 0	23	13 and 14 and 16 and 17 and 18 and 19	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/02/ 02 19:19			